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APPLICATION NO.	FILING DATE		FIRST NAMED INVEN	TOR		ATTO	RNEY DOCKET NO.	со	NFIRMATION NO.	
10/829,380 TITLE OF INVENTION	04/22/2004 I: SEMICONDUCTOR I	NTEGRATED CIRCUIT	Atsuhiro Hayashi CONTROLLING OL		T IMPEDANCE A	AND SI	H-1139 LEW RATE	-	7082	
			·				HMARZI2 00000024	108	* ***** 1:11 1111	
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE D	UE	PREV. PAID SSU	注號	TOTAL FEE(S) DUE		JOATE DUE	
nonprovisional	NO NO	\$1400	\$300		\$0		\$1700		12/26/2006	
EXAMINER		ART UNIT	CLASS-SUBCLASS							
LAM, TUAN THIEU		2816	327-108000							
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PLEASE NOTE: Unl recordation as set fort (A) NAME OF ASSIG	less an assignee is identi h in 37 CFR 3.11. Comp	A TO BE PRINTED ON ified below, no assignee oletion of this form is NO	data will appear on the	ne pa g an a	tent. If an assignossignment. and STATE OR C			ocume	ent has been filed for	
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	s SMALL ENTITY startu	is. See 37 CFR 1.27.	☐ b. Applicant is no	long	er claiming SMAL	L ENT	TTY status. See 37 CI	FR 1.2	27(g)(2).	
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